

A Non-galvanic D-band MMIC-to-Waveguide Transition Using eWLB Packaging Technology

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Abstract—This paper presents a novel D-band interconnect implemented in a low-cost embedded Wafer Ball Grid Array (eWLB) commercial process. The transition is realized through a patch slot antenna directly radiating to a standard waveguide opening. The interconnect achieves low insertion loss and good bandwidth. The measured minimum Insertion Loss (IL) is 2 dB and the average is 3 dB across a bandwidth of 22% covering the frequency range 110-138 GHz. In addition, the structure is easy to integrate as it does not require any special assembly nor any galvanic contacts. Adopting the low-cost eWLB process and standard waveguides makes the transition an attractive solution for interconnects beyond 100 GHz.

Index Terms—D-band, interconnects, waveguide transition, eWLB, millimeter waves, THz.

I. INTRODUCTION

The ever increasing advance in semiconductor technologies makes mm-wave technologies very attractive for several wireless applications from telecom to safety, production quality check and several other applications [1].

In this context, one of the biggest challenges that researchers face, is the realization of low-loss and low-cost interconnection and high-level integration. Various approaches are proposed in literature in order to couple the RF signal to the MMIC at mm-wave frequencies. One possibility is the integration of the antenna on chip, which is a very compact solution with the drawback of low antenna efficiency and limited bandwidth since most of the broad-side antennas are resonant structures [2].

Another option is to couple the MMIC directly to a waveguide and hence achieve efficient coupling over broader frequency range. However, most highly integrated circuits are relatively large in size with respect to the wavelength and therefore the integration of MMIC to waveguide transitions

needed not only because of the area limitation but also to prevent waveguide modes from leaking into the circuit cavity. The drawback of this solution is that it requires a bond-wire interface between the waveguide-transition and the MMIC. The use of bondwires is not suitable at such high frequencies since they introduce inductive effects and require special measures to provide reasonable return-loss for the interface [2].

Embedded wafer level ball grid array (eWLB) provides an attractive solution for packaging MMIC into a ball grid array

surface mountable module with hundreds of I/O connections. Low frequency I/O has been proposed and studied in [3] using standard eWLB connections. High frequency I/O connection up to 100 GHz has been proposed using antenna-like structures [3]. To the authors' knowledge, sub mm-wave interconnects on eWLB technology has not been proposed before.

In this work, a generic approach for a MMIC-to-waveguide transition based on eWLB process is proposed with the support of experimental results. The choice of eWLB technology is motivated by the need of a low-cost and high-volume process for interconnects that operate at mm-wave frequencies. This has been a major challenge hindering commercialization of mm-wave systems. To the authors' knowledge, this is the first attempt to fabricate a transition beyond 100 GHz using eWLB process.

II. TRANSITION DESIGN

The proposed solution consists of an eWLB chip with an embedded MMIC, Ball grid array (BGA) for low frequency I/Os, eWLB antenna for I/Os above 100 GHz, a PCB to support eWLB and a standard air-filled metal waveguide. The complete solution is shown in Fig. 1. The eWLB process provides two metal layers named redistribution layers (RDL), mainly used for I/O connections. In this work, the redistribution layers are also used to realize the antenna that couples the signal into the waveguide avoiding galvanic connection. The dielectric constant of the substrate is 3.2 which is suitable for limiting the leakage into the substrate. The substrate height is 0.45 mm which is close to quarter wavelength at D-band and hence, placing the chip on a

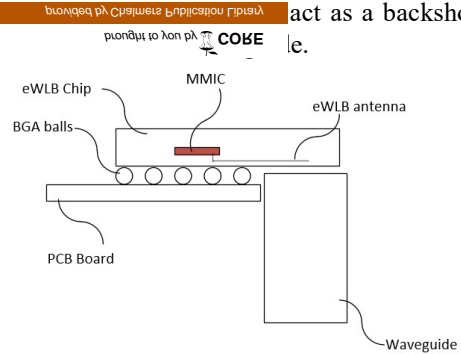


Fig. 1. Simplified schematic for the proposed solution

The patch slot antenna is implemented on the top redistribution layer. The antenna's slot width is 0.15mm. The antenna faces the waveguide opening which is mounted perpendicular to the eWLB level. The antenna feed lines are implemented as coplanar waveguide (CPW) lines. The ground lines are implemented on the top redistribution layer and the signal feed line is implemented on the bottom redistribution layer to avoid shorting the signal to ground in case the waveguide walls made contact with the eWLB's surface. The CPW lines can be used to connect the transition to any MMIC.



Fig. 2. Waveguide's 90° bend with one end connected using a flange and the other end facing the eWLB

A commercial D-band waveguide 90° bend was used to test this design. A 30-um-depth slot was machined in the waveguide wall above the feeding lines as shown in Fig. 2 in order to avoid disturbing the CPW mode.

The sides of the chip were metallized by machining a channel through a metal base with the same height as the chip. Metalizing the side of chip resulted in an improvement of 0.5 dB in insertion loss.

Fig. 3 shows a photo of the fabricated prototype. The chip is implemented as a back-to-back solution allowing straightforward testing and calibration. Alignment markers were used on-chip to correctly align waveguide openings to the antenna to achieve maximum coupling. CPW line structures were also added on chip to calibrate line losses and extract the transition loss accurately.

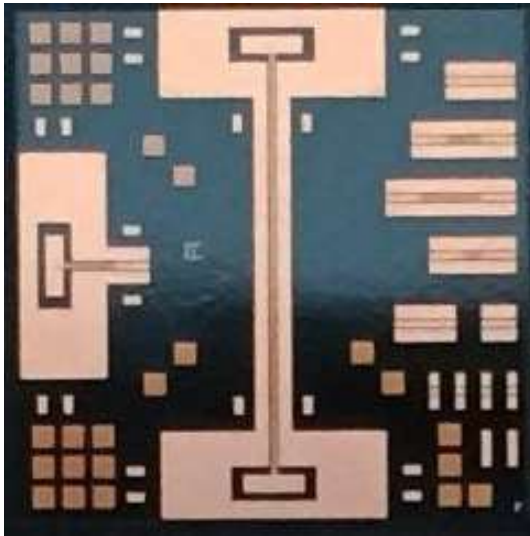


Fig. 3. Chip photo

III. EXPERIMENTAL RESULTS AND DISCUSSION

Measurements were performed in a back-to-back fashion. The waveguide bends were mounted on a probe station in order to provide accurate alignment of the waveguide openings. The measurement structure is based on a PNA-X network analyzer up to 67 GHz, a pair of frequency extension modules to up/down-convert the signal frequency to D-band. The measurement setup is shown in Fig. 4. Two-port calibration was performed till the inputs of the waveguide bends.



Fig. 4. Measurement setup

The losses of the bends were measured separately and de-embedded manually. Measurements show a loss of 0.9 dB per bend at D-band. De-embedded measurement results showed an average insertion loss of only 3 dB per transition as reported in Fig. 5. The minimum achieved insertion loss is 2 dB. The transition shows a relatively wide bandwidth of 22% covering the range 110 to 138 GHz. Simulations have been performed using HFSS 3D EM simulator and compared to

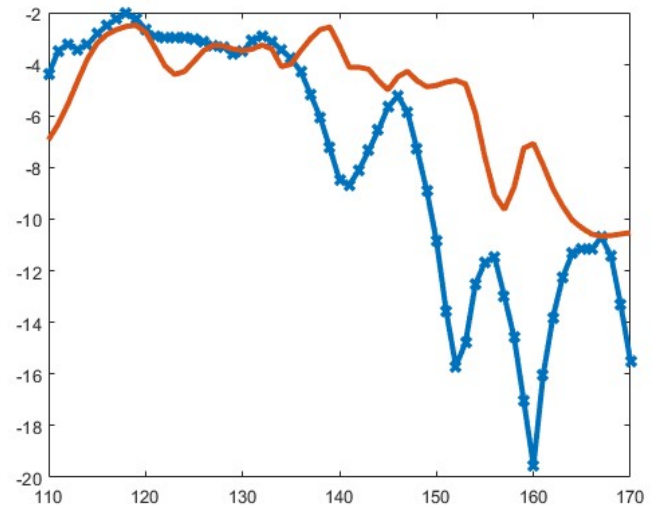


Fig. 5. Insertion loss (Simulations — vs. Measurements —x—)

TABLE I
COMPARISON OF DIFFERENT MMIC-TO-WAVEGUIDE TRANSITIONS

Ref.	Description	Frequency (GHz)	Insertion loss (dB)	Bandwidth	Technology
[4]	Micromachined Sub-THz Interconnect Channels	151-160	9	5.7%	Micromachined Si
[5]	Dielectric waveguide	146-198	4.9	30%	HR Si
[6]	Dielectric waveguide	140-156	7	10.8%	PMMA dielectric waveguide
This work	Non-galvanic antenna based transition	110-138	3	22%	eWLB

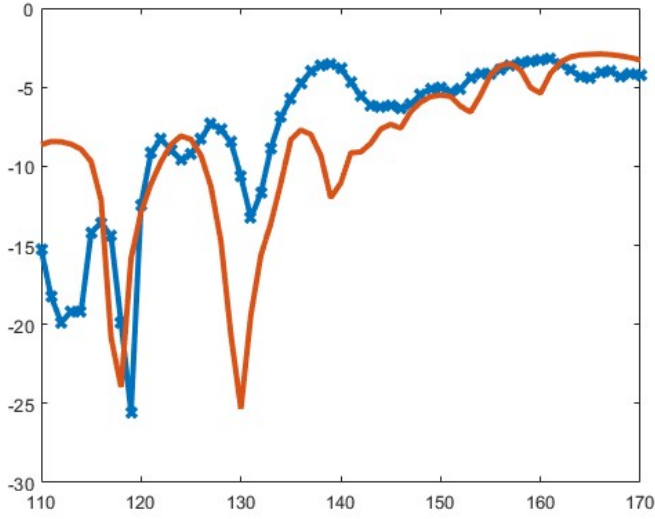


Fig. 6. Return loss at port 1 (Simulations — vs. Measurements —x—)

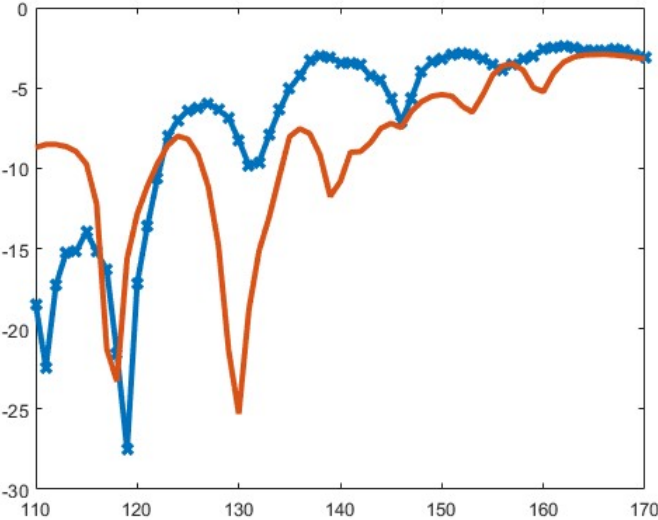


Fig. 7. Return loss at port 2 (Simulations — vs. Measurements —x—)

measurements. It is noteworthy that the measured frequency response shows a shift towards lower frequencies compared to simulated response. The return losses at both ends are shown in Fig. 6 and Fig. 7. Simulations show good agreement with measurements especially for nulls locations. The performance of the proposed transition is compared to similar work at the same frequency in Table 1. The comparison shows that the proposed transition has the lowest insertion loss and a relatively wide bandwidth.

IV. CONCLUSION

A novel D-band MMIC-to-waveguide transition on a commercial low cost eWLB process has been presented. The transition is implemented using a patch slot antenna radiating to a standard waveguide. Experimental results demonstrate that the transition achieves low insertion loss of 3 dB and a bandwidth of 22% at D-band. The transition does not require galvanic contacts and forms a generic interconnect that can be integrated with any MMIC/PCB. The proposed transition represents a solid step towards high volume commercialization of mm-wave and THz systems as it is implemented in a well-established commercial technology.

Acknowledgment

This project has received funding from the European Union's Horizon 2020 research and innovation program under grant agreement No. 644039.

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